

	Type	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	2	improv\$3 with edge\$1 with signal\$1 with (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 09:07	
2	BRS	2	improv\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:30	
3	BRS	18	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:30	
4	BRS	1	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or digital with processing with system\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:31	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	3	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or digital with process\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:33	
6	BRS	8	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:33	
7	BRS	0	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and (calculat\$3 or comput\$3) with error\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:34	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
8	BRS	5	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and (calculat\$3 or comput\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:34	
9	BRS	3	tim\$3 with edge\$1 with signal\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and calculat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:35	
10	BRS	3	tim\$3 with edge\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and calculat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 07:35	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	4	tim\$3 with edge\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and error\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:29	
12	BRS	4	tim\$3 with edge\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and accurac\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:29	
13	BRS	21	tim\$3 with edge\$1 same accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:30	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	13	tim\$3 with edge\$1 with accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:32	
15	BRS	9	tim\$3 with edge\$1 with accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with signal\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:35	
16	BRS	10	tim\$3 with edge\$1 with accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:35	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
17	BRS	1	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 with accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:36	
18	BRS	12	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 and accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:37	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	4	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 and accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and instruct\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:37	
20	BRS	4	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 and accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and (command\$3 or instruct\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:38	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
21	BRS	5	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 and accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and error\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:40	
22	BRS	12	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 and accurac\$3 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:41	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
23	BRS	15	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 and (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:41	
24	BRS	0	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 same (DUT or device\$1 adj under adj test) with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:43	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
25	BRS	4	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 same (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1 and (DUT or device\$1 adj under adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 08:50	
26	BRS	4	(calibrat\$3 or adjust\$3) with tim\$3 with edge\$1 same (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1 and (DUT or device\$1 adj under adj test) and (reference\$1 or threshold\$1 or desired or expected or predefined or predetermined or preset\$4) with tim\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 09:09	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
27	BRS	2	"20050044463"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/11/02 08:53	
28	BRS	595	702/57.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/11/02 09:08	
29	BRS	208	702/69.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/11/02 09:08	
30	BRS	55	702/69.cccls. and tim\$3 with edg\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/11/02 09:08	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
31	BRS	4	(calibrat\$3 or adjust\$3) with tim\$3 with edg\$3 same (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1 and (DUT or device\$1 adj under adj test) and (reference\$1 or threshold\$1 or desired or expected or predefined or predetermined or preset\$4) with tim\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 09:09	
32	BRS	28	(calibrat\$3 or adjust\$3) with tim\$3 with edg\$3 and test\$3 with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1 and (DUT or device\$1 adj under adj test) and (reference\$1 or threshold\$1 or desired or expected or predefined or predetermined or preset\$4) with tim\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 10:08	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
33	BRS	20	(calibrat\$3 or adjust\$3) with tim\$3 with edg\$3 and test\$3 with (integrated adj circuit\$1 or IC\$1) and (DPS or (data or digit\$2) with process\$3) and cycle\$1 with tim\$3 and clock\$1 and (DUT or device\$1 adj under adj test) and (reference\$1 or threshold\$1 or desired or expected or predefined or predetermined or preset\$4) with tim\$3 and computer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 11:24	
34	BRS	3571	714/724,742,744.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 11:25	
35	BRS	5185	324/158.1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/02 11:25	
36	BRS	787	data adj acquisition adj system with (processor\$1 or microprocessor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/11/03 07:56	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
37	BRS	77	"702"/\$.cccls. and data adj acquisition adj system with (processor\$1 or microprocessor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/11/03 07:56	